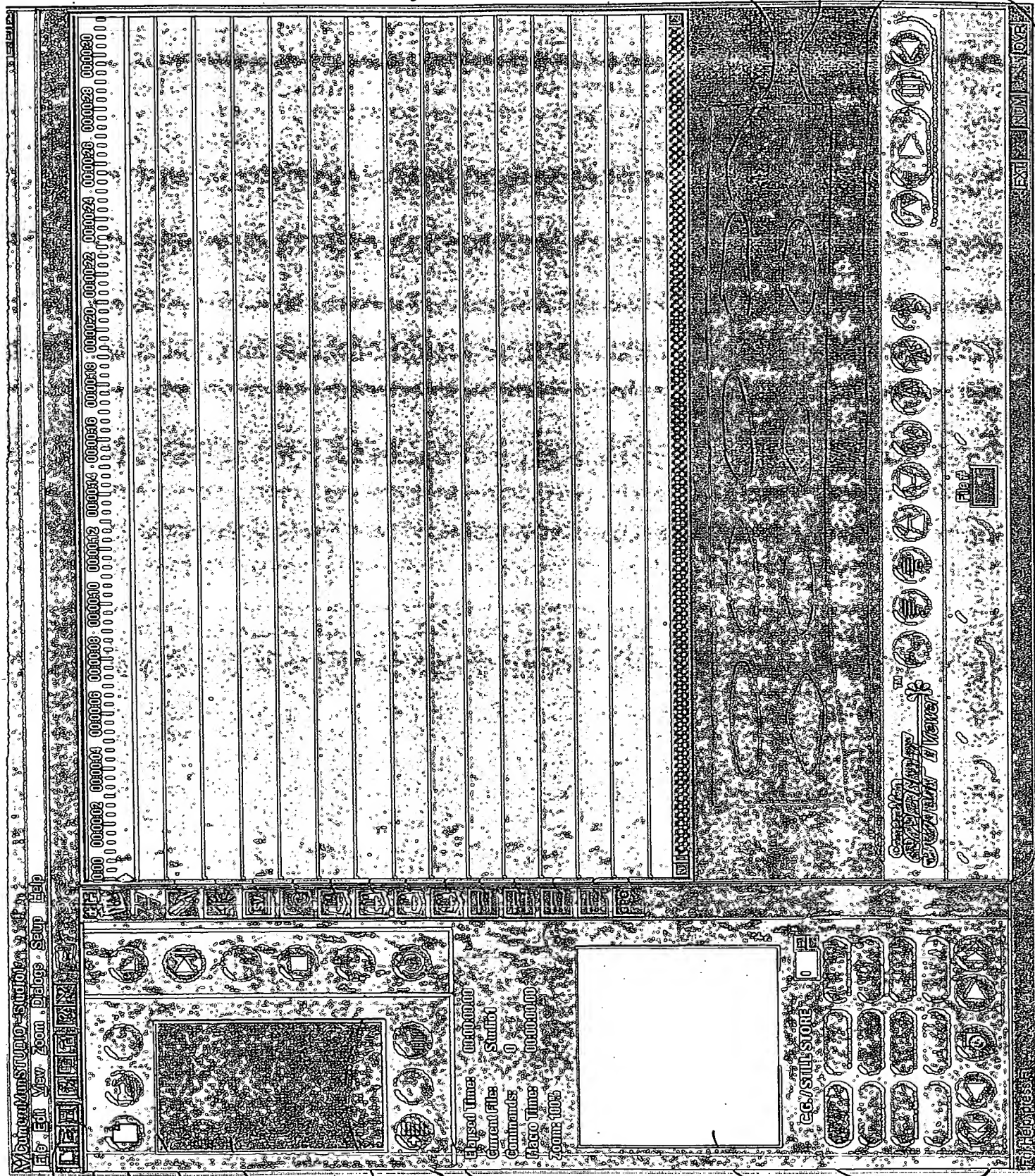


FIG. 1

133



249

250

265

264

262

266

260

261

211

210

299

210

212

FIG. 2B

208

202

303

916

Y13.

-310

-311

-312

-313

FIG. 3.

204

401 402 403 404 405 406 407 408 410

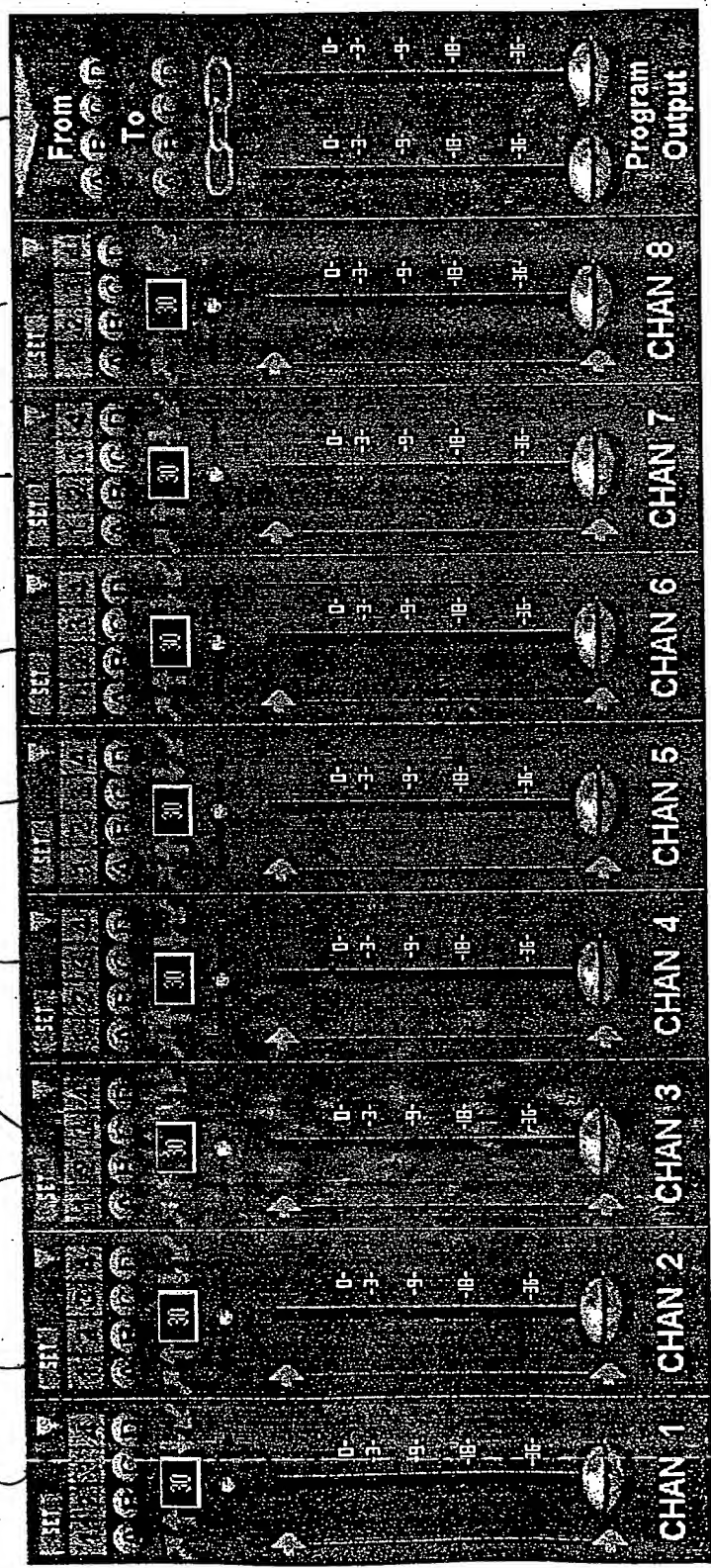


FIG. 4

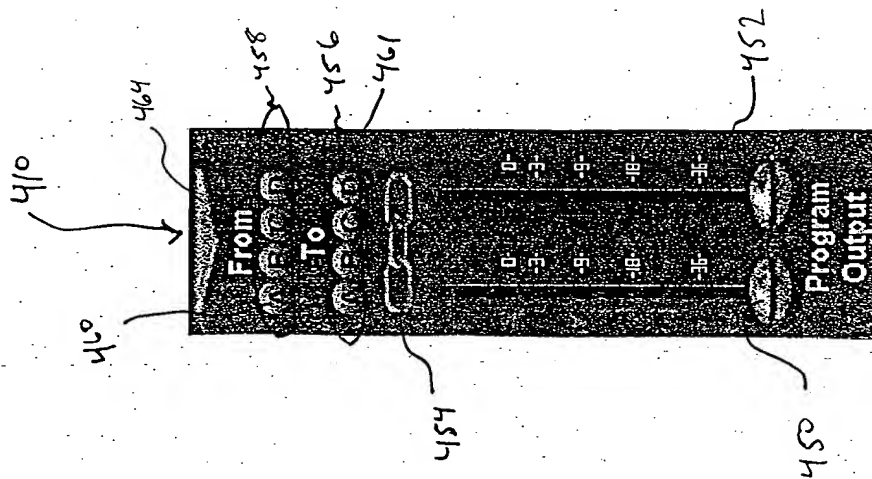


FIG. 4A

FIG. 4A

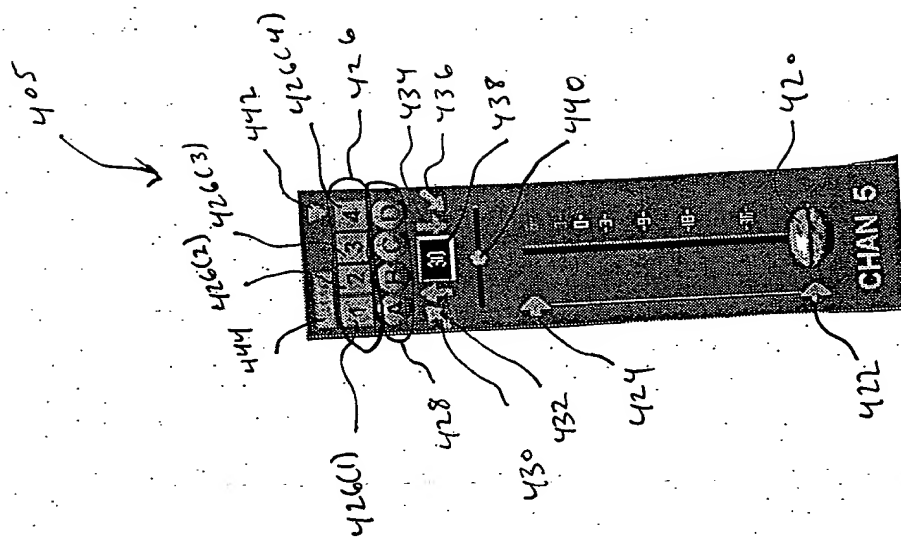


FIG. 4B

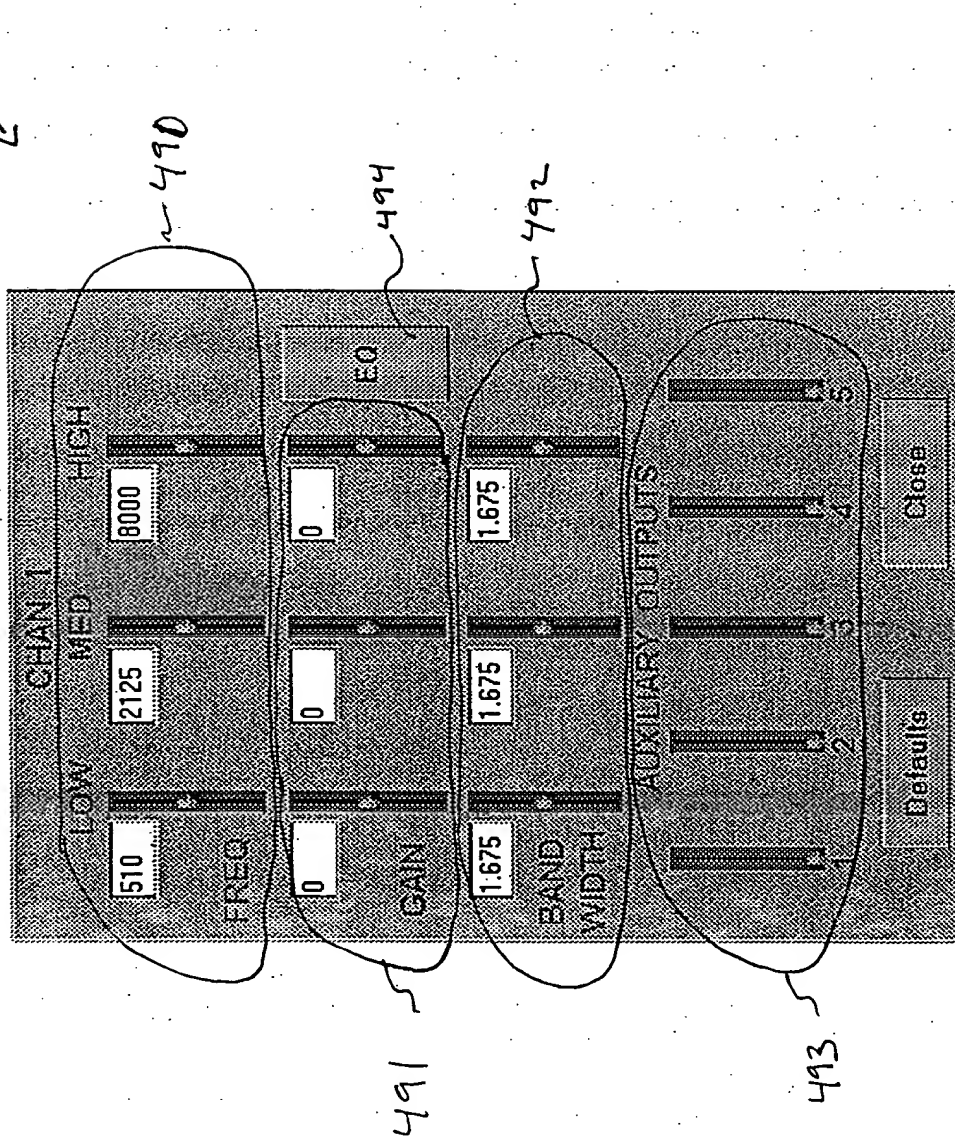
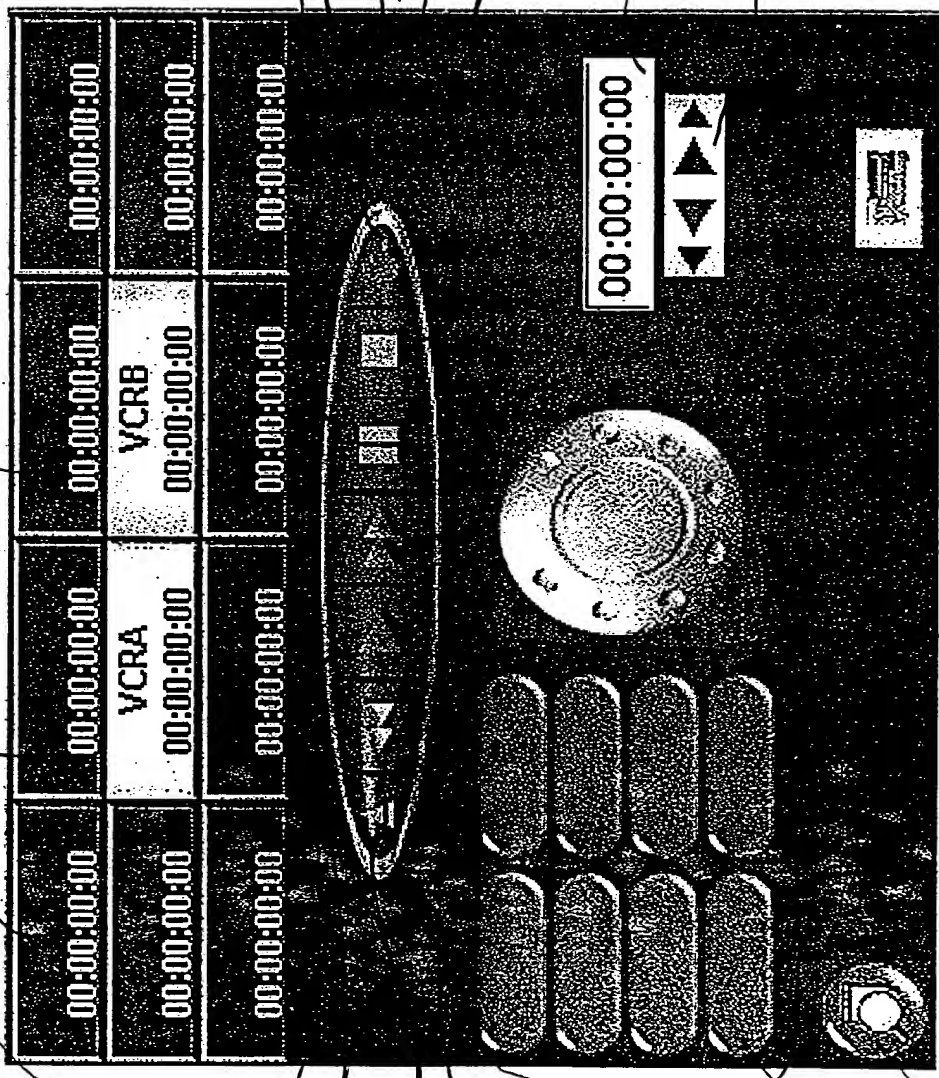


FIG. 4C

FIG. 5 is a block diagram of a system 500 for processing video data. The system 500 includes a video source 502, a video processor 504, a video storage device 506, and a video display device 508. The video source 502 is connected to the video processor 504, which is connected to the video storage device 506. The video storage device 506 is connected to the video display device 508. The video processor 504 includes a video input interface 510, a video processing unit 512, and a video output interface 514. The video input interface 510 is connected to the video source 502. The video processing unit 512 is connected to the video storage device 506. The video output interface 514 is connected to the video display device 508. The video source 502 includes a video input interface 510, a video processing unit 512, and a video output interface 514. The video input interface 510 is connected to the video source 502. The video processing unit 512 is connected to the video storage device 506. The video output interface 514 is connected to the video display device 508.

206

502(1)
502(2)
502(3)



514
510
512
524
525
526
527
528
529
530
532

FIG. 5

2155 29 25

546

FIG. 5A

808

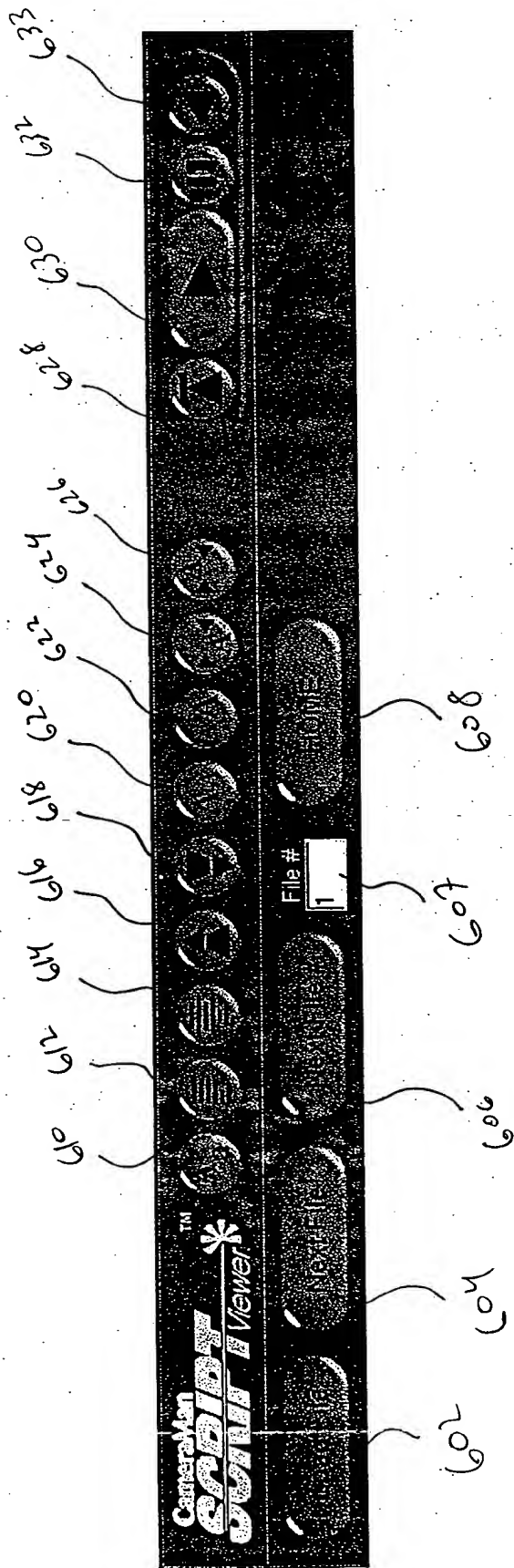


FIG. 6

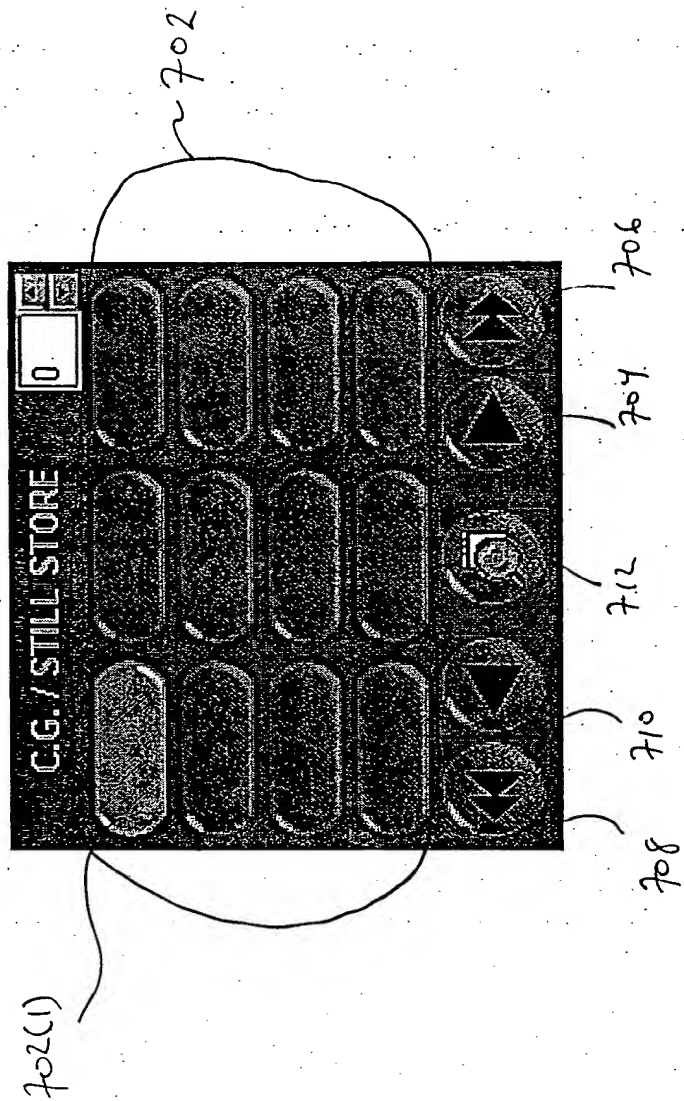


FIG. 7

724 ☒ Read

726 ☐ Buffer

Page number 125 722

CG ID 1 723

Auto Font Y 727

Description "page 125" 728

730 OK

732 Cancel

FIG. 7A

FIG. 8 is a perspective view of the device 205 in a closed position.

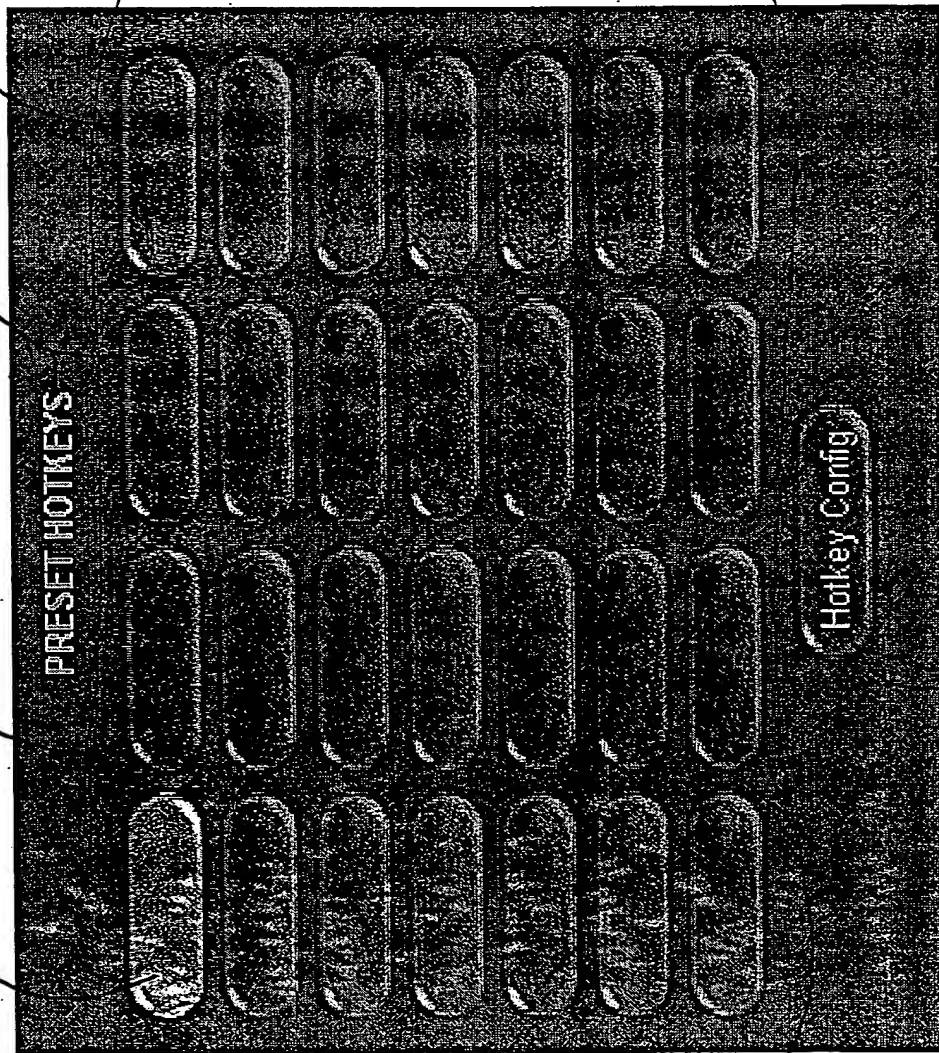
205

802(1)

802(2)

802(3)

802(4)



802

FIG. 8

804

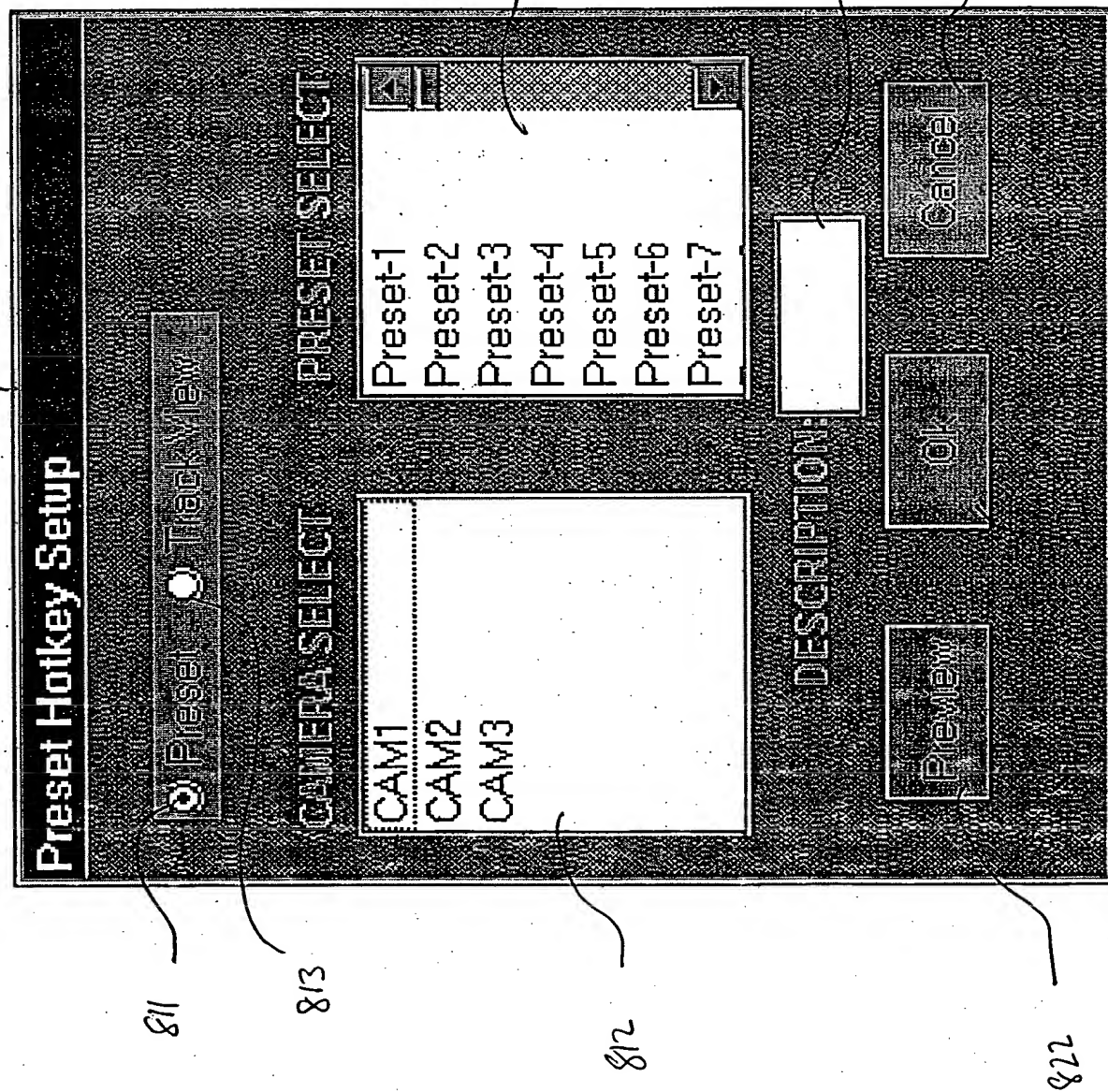


FIG. 8A

001010 02553400

203

902

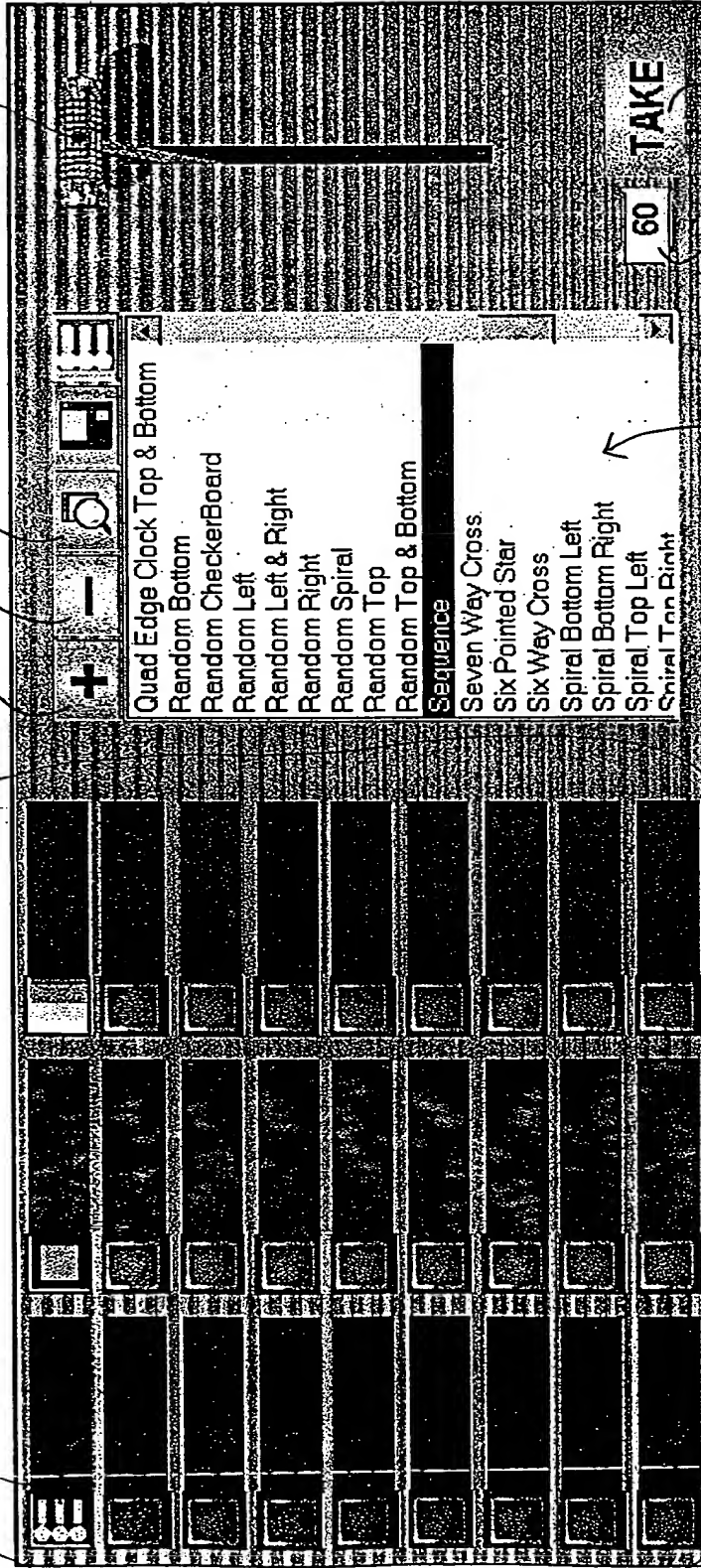
908

916

914

912

90206



906

904

910

FIG. 9

954 960 956 950 958

Wipe

☒ Background

☐ Clear All Previous Process Effects

☐ DVE On

☐ DSK On

Double Click Mouse on Choice

Frame	Wipe	Test	Border	Thickness	X Pos	Y Pos	Rotation	Spin
0								0
0								0
0								0
0								0
0				2886				0
0								0
0								0

Single
DSK DVE

OK

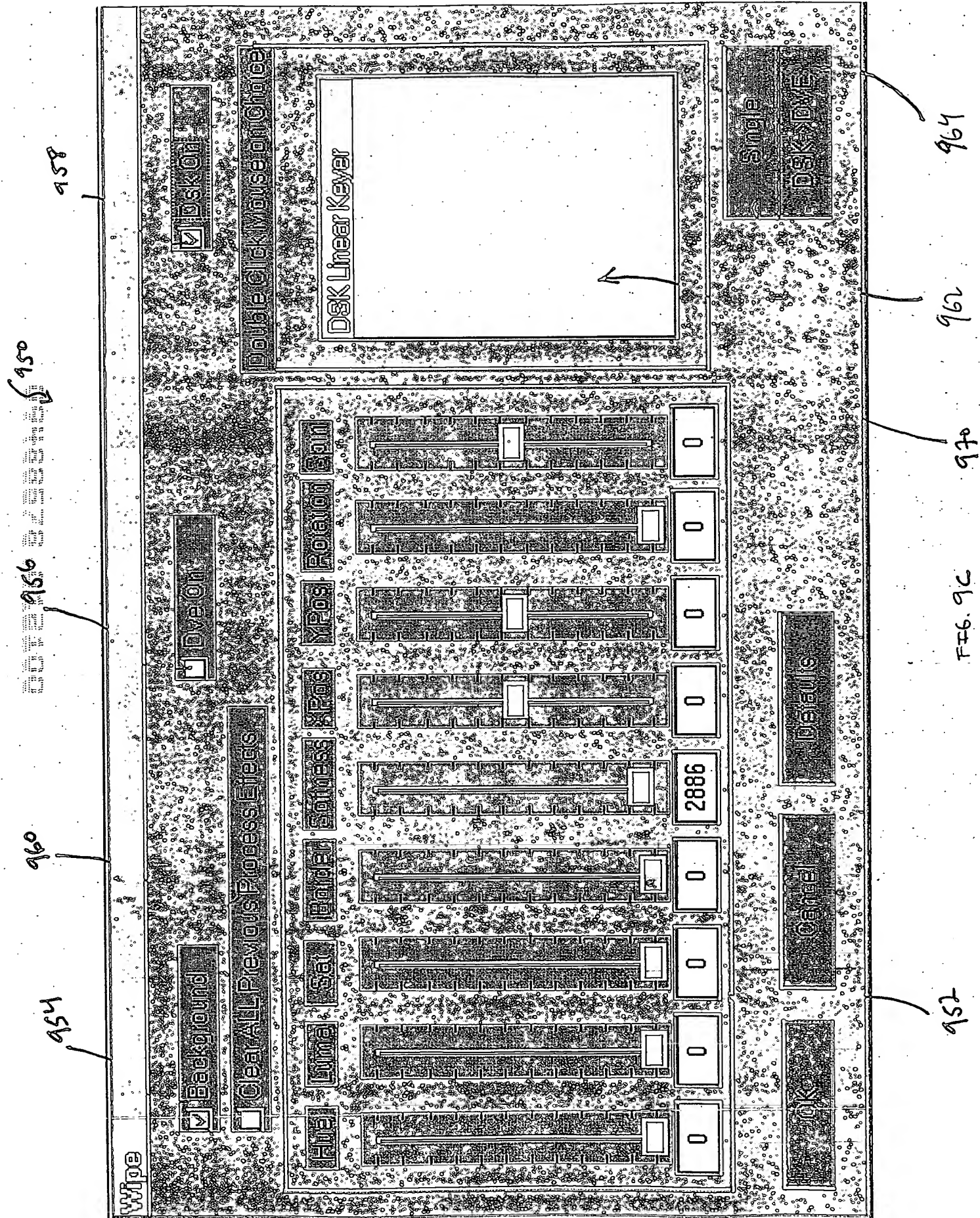
Cancel

Details

Double Click Mouse on Choice

952 970 969

FIG 9A



00:00:00 299

1024 1025 1026 1027 1028 1029

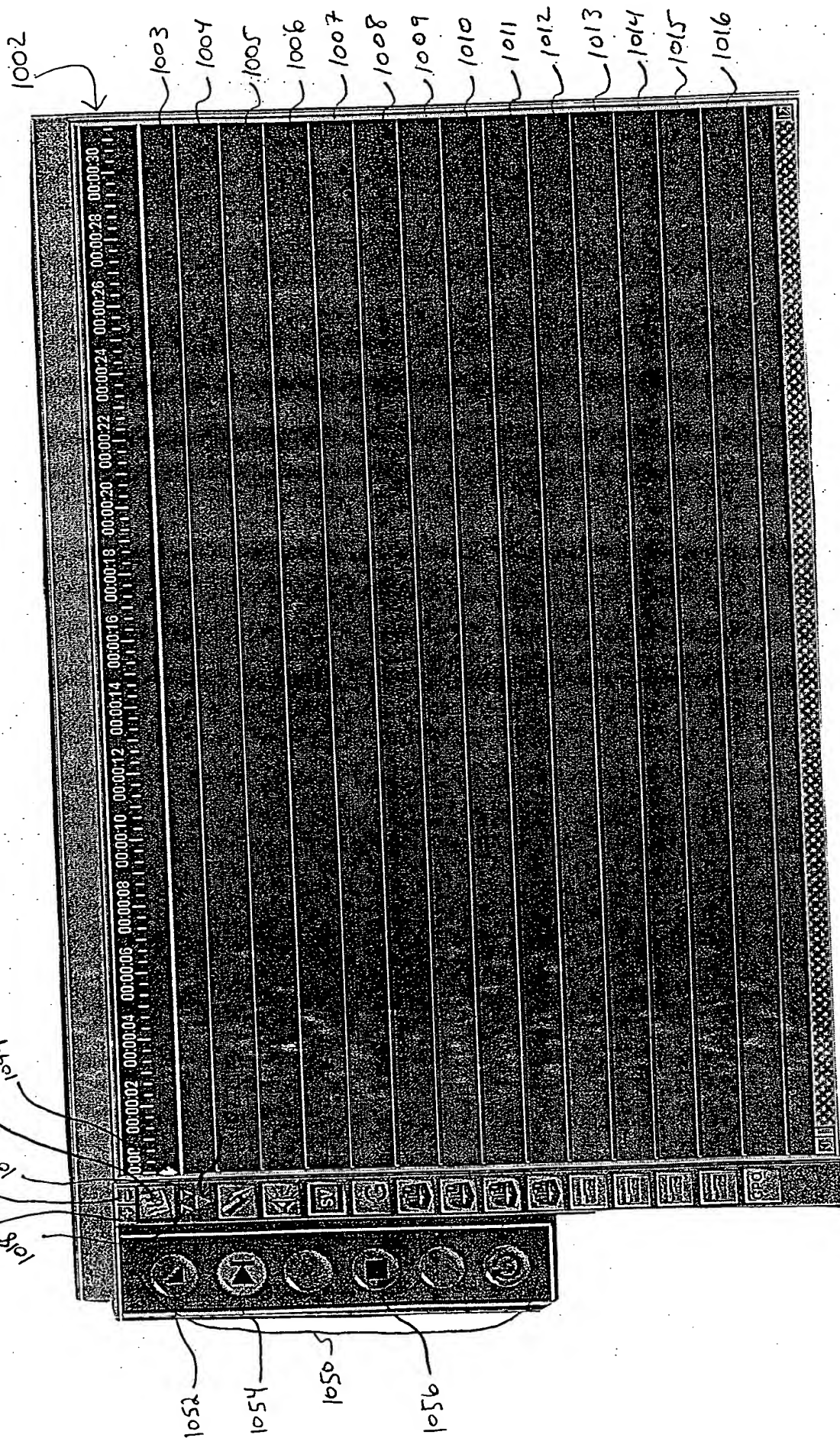


FIG. 10

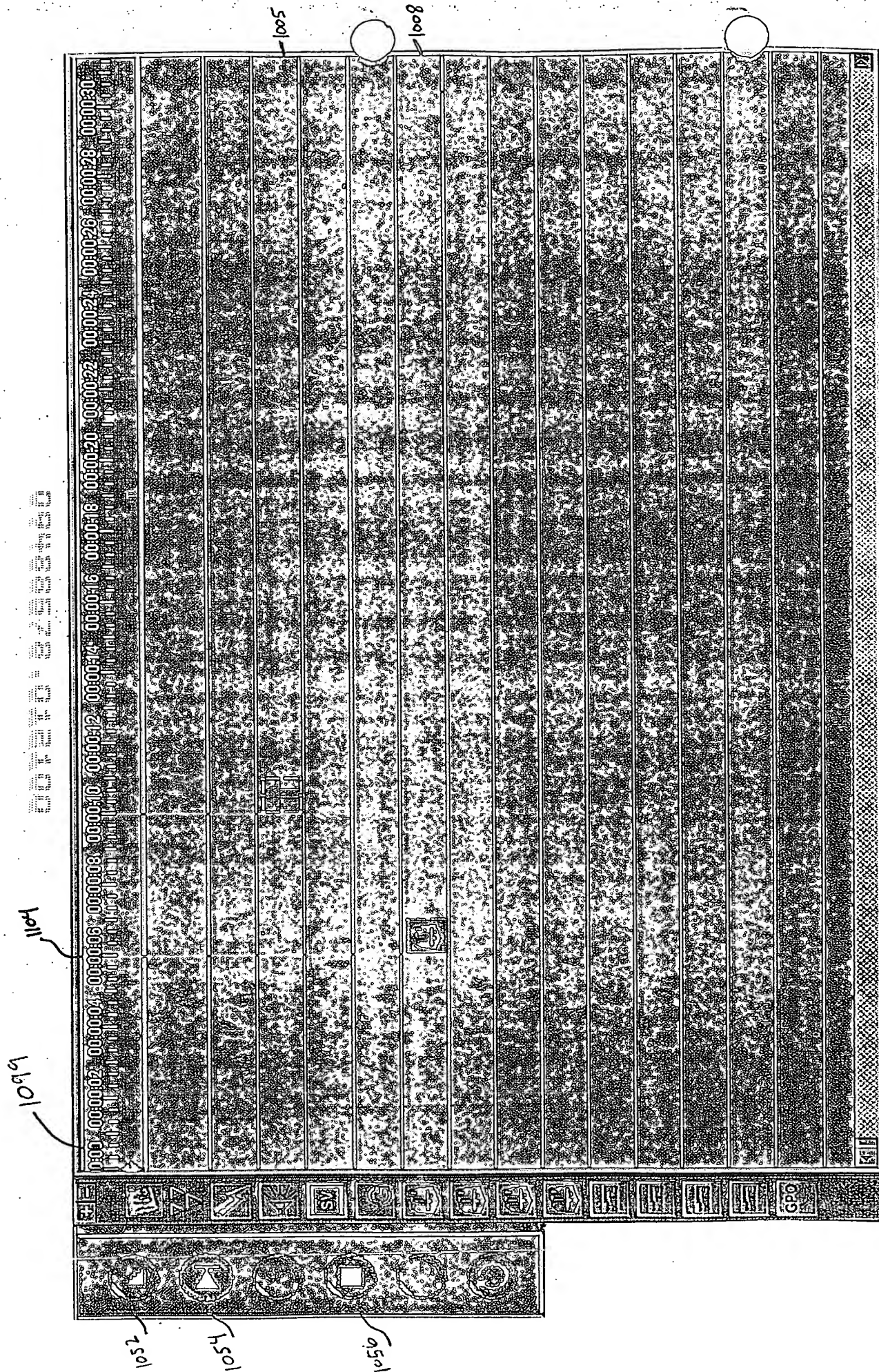


FIG. 11

1200

Start time 180

stop time 220

Preset Preset #1

ok

Cancel

FIG. 12

1300.

FIG. 13

FIG. 13

[illegible]

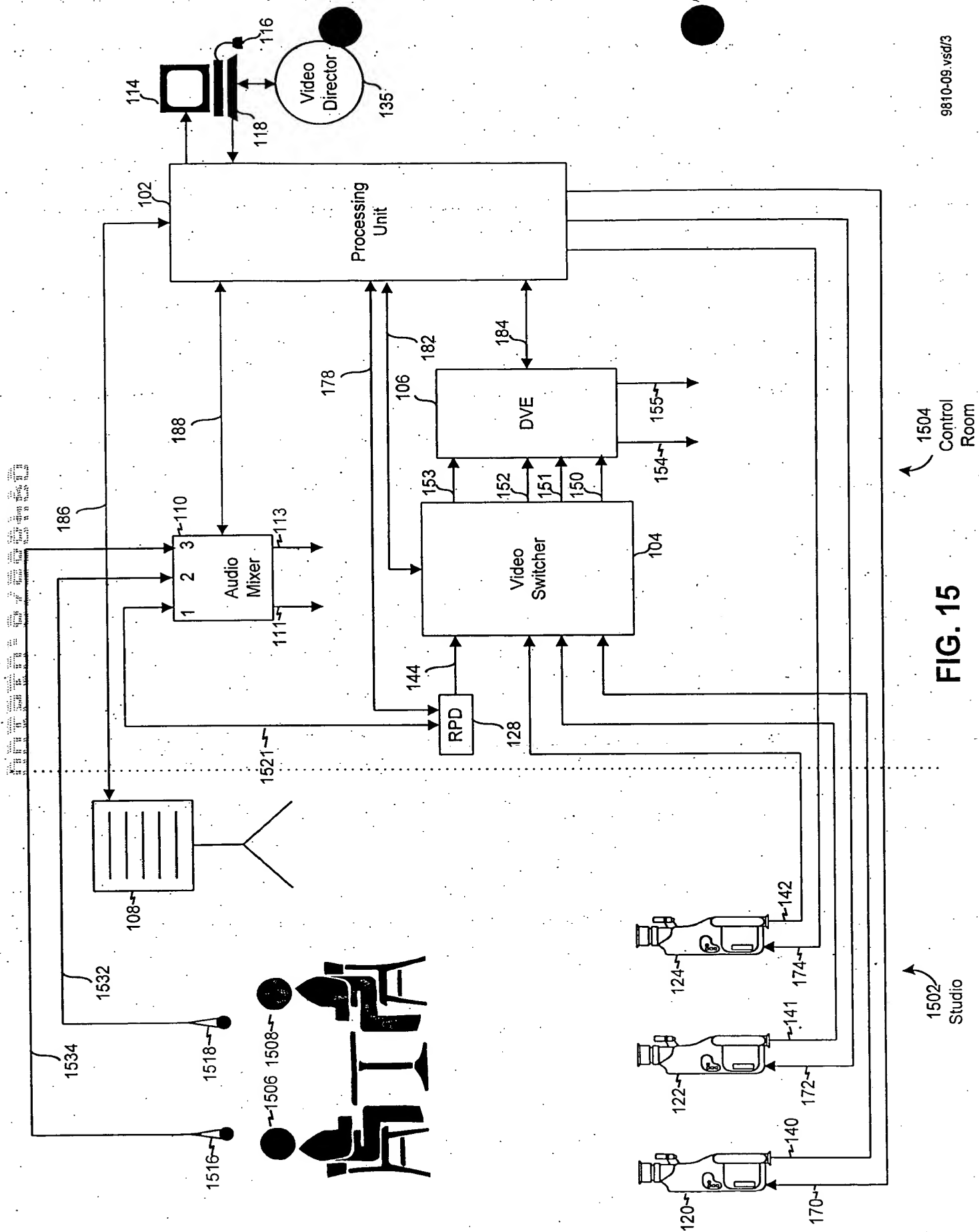


FIG. 15

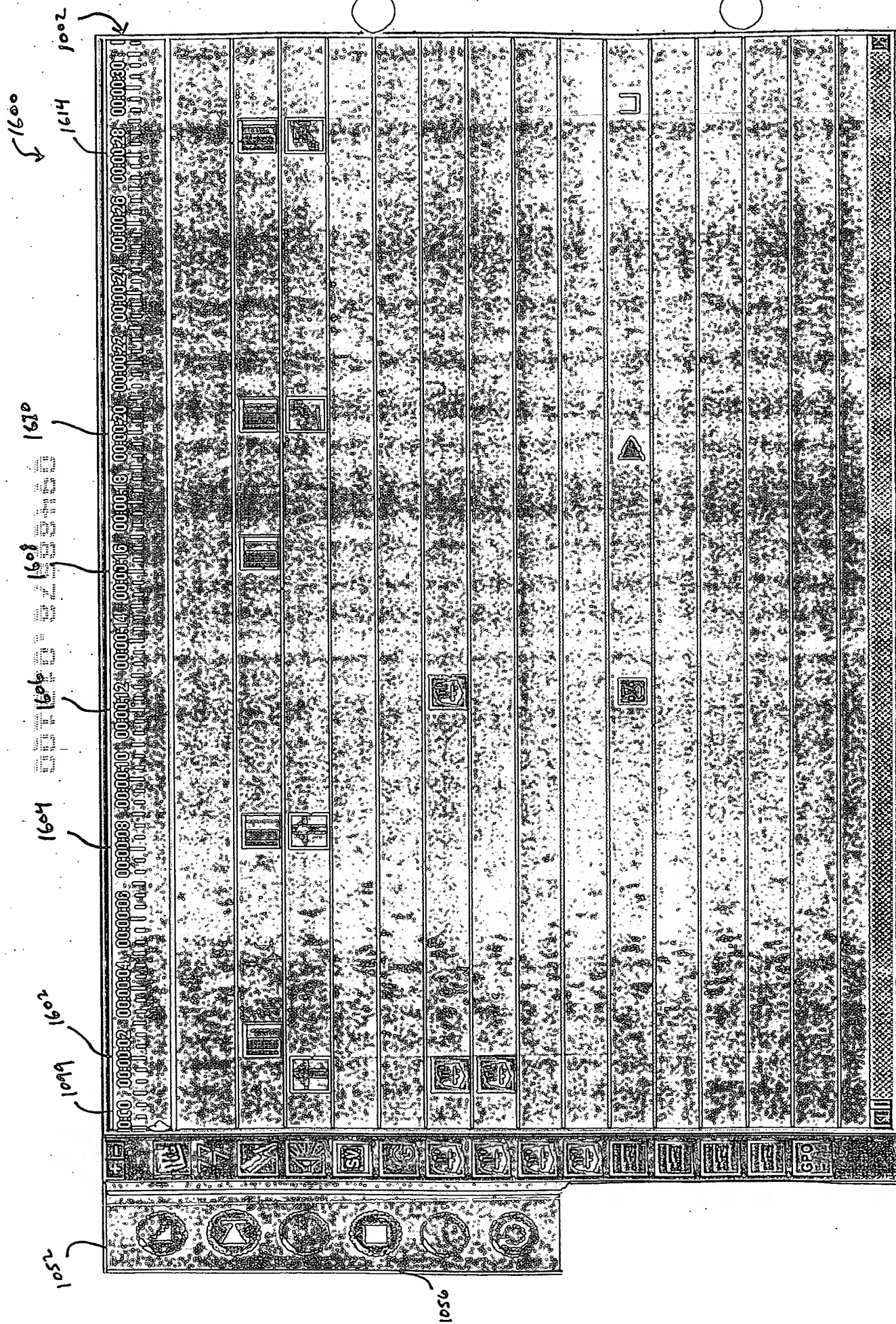


FIG. 16

671

FIG. 17


FIG. 17

00:00:16.00 ~ 1802

30 ~ 1804

1806

Cross Fade down



chan 5

None

1812
A

18/21

A

Chan 2

3

Yes.

Ma

FILE 18

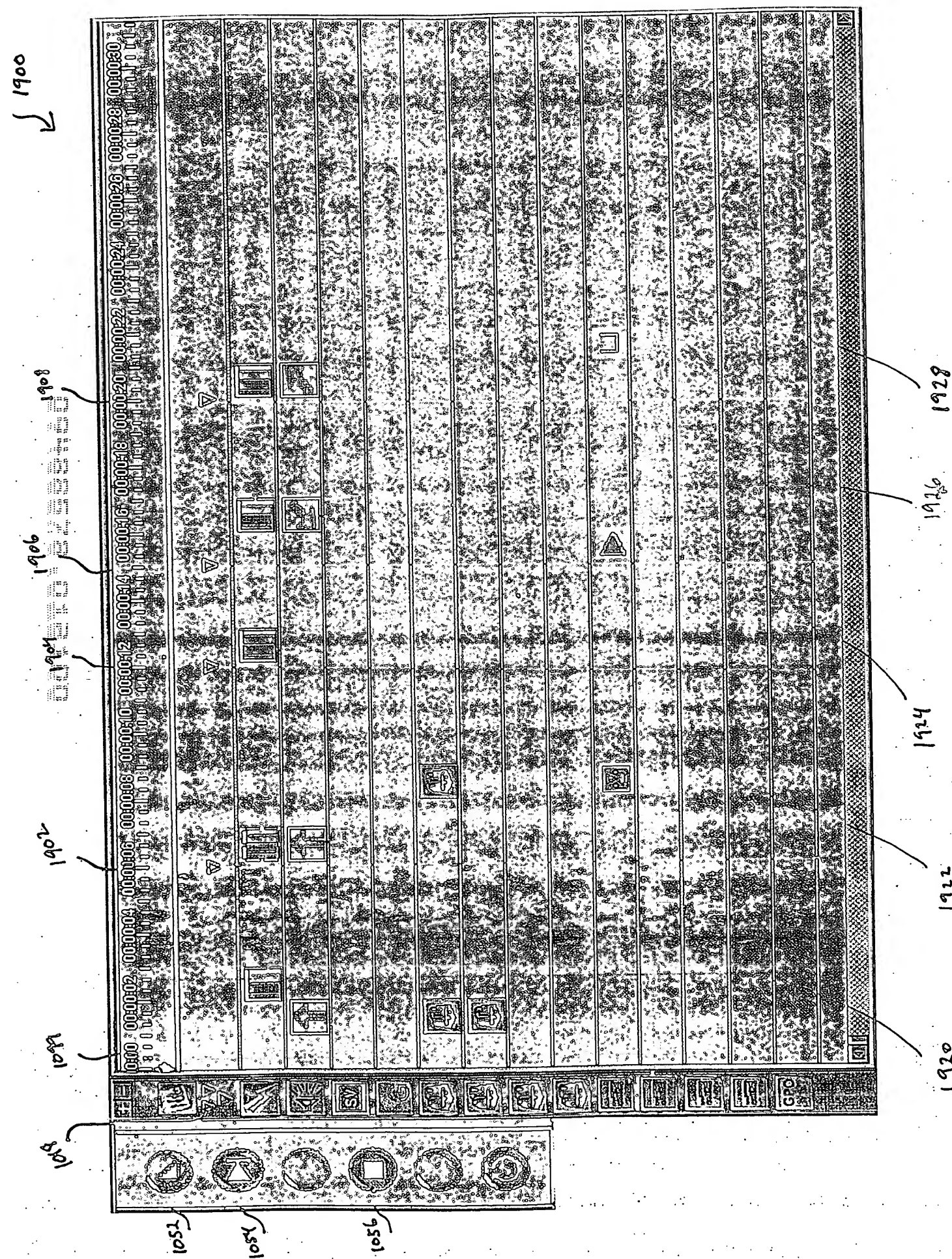


FIG. 19

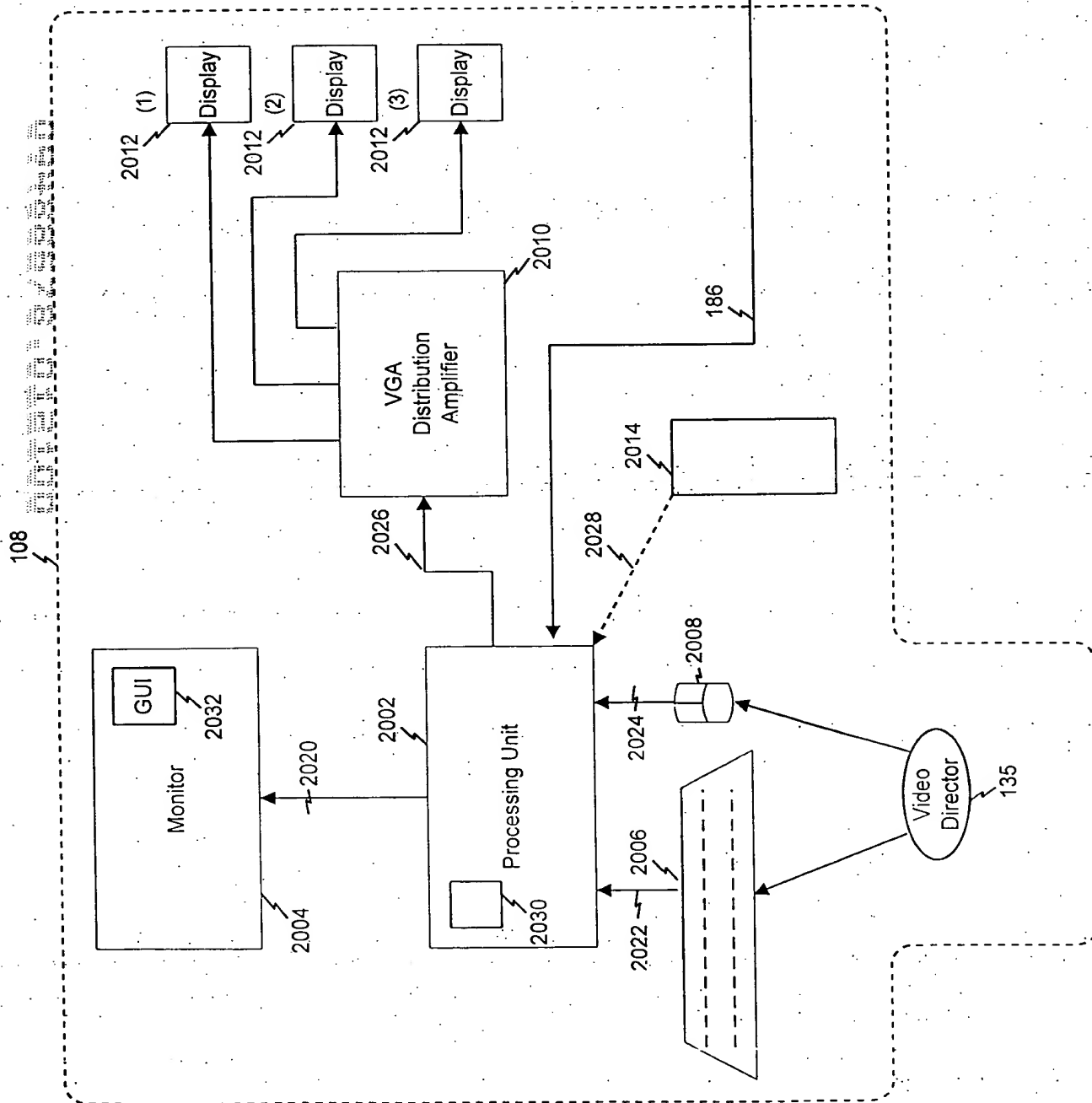


FIG. 20

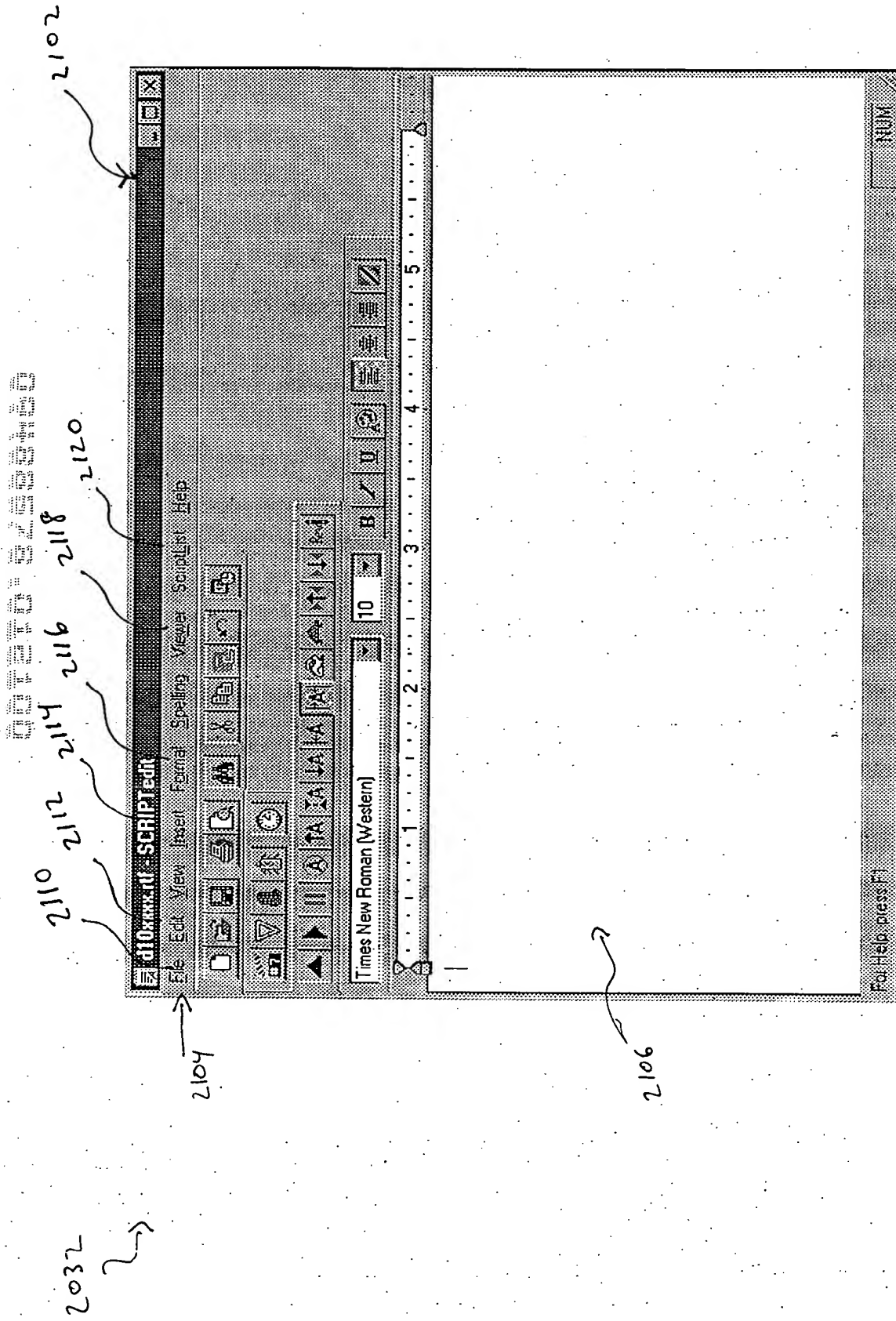
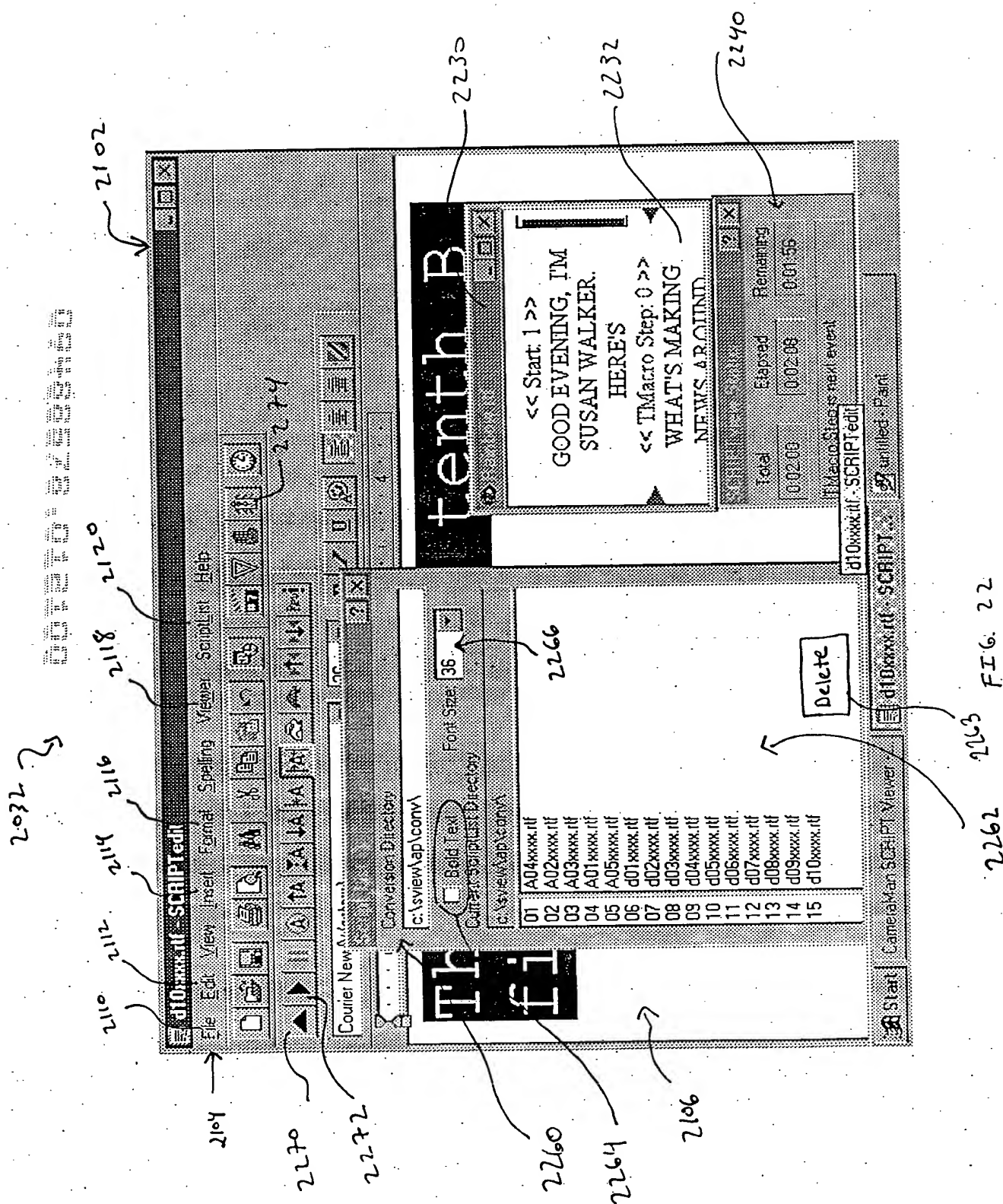


FIG. 21



2012

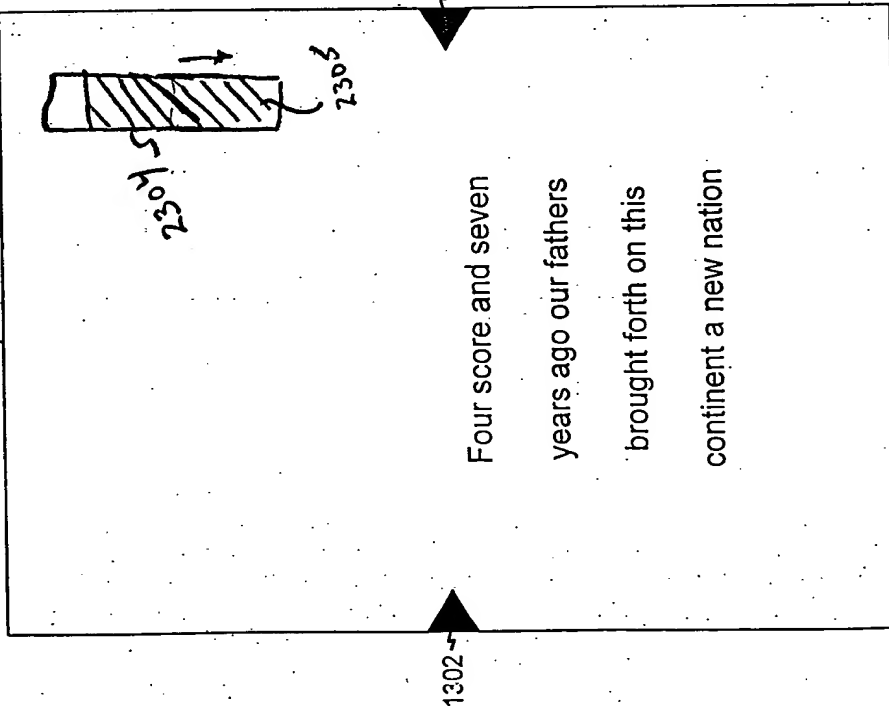


FIG. 23A

2012

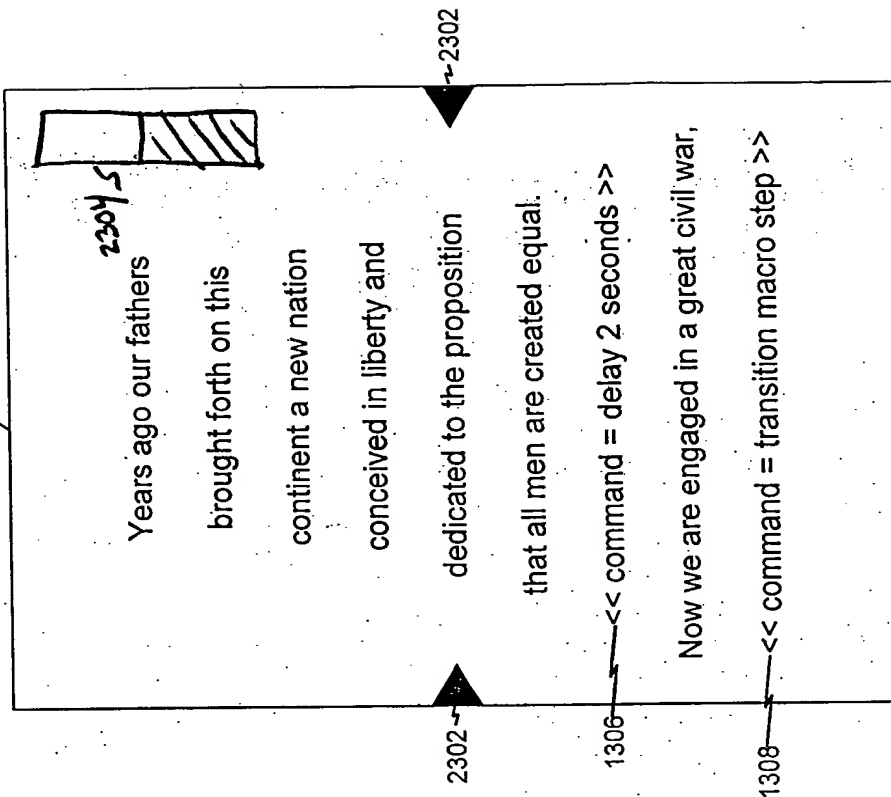


FIG. 23B

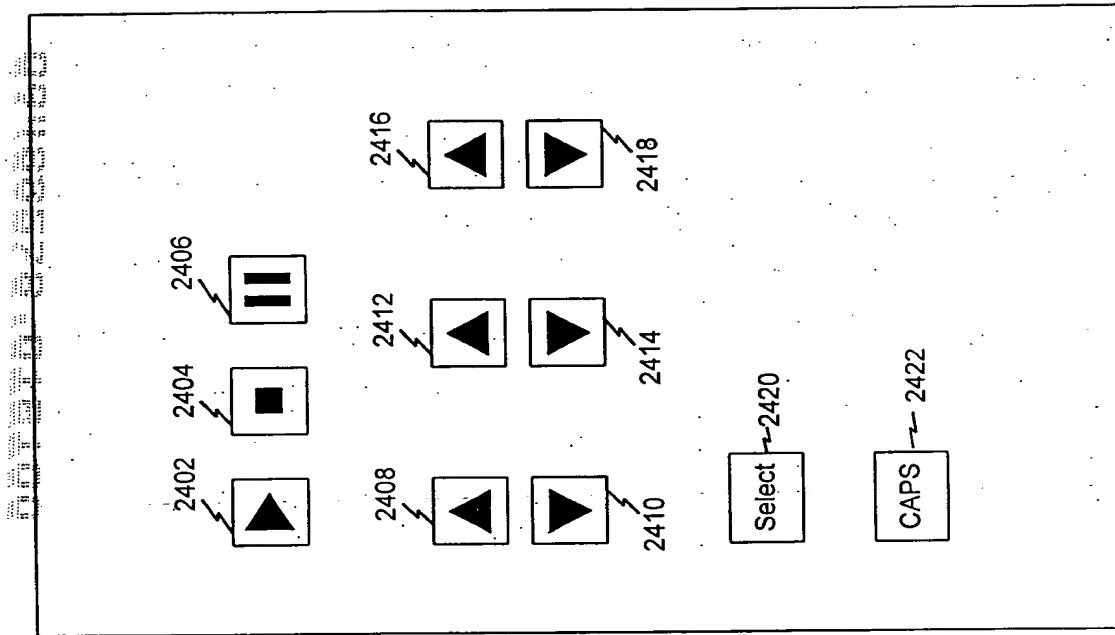


FIG. 24

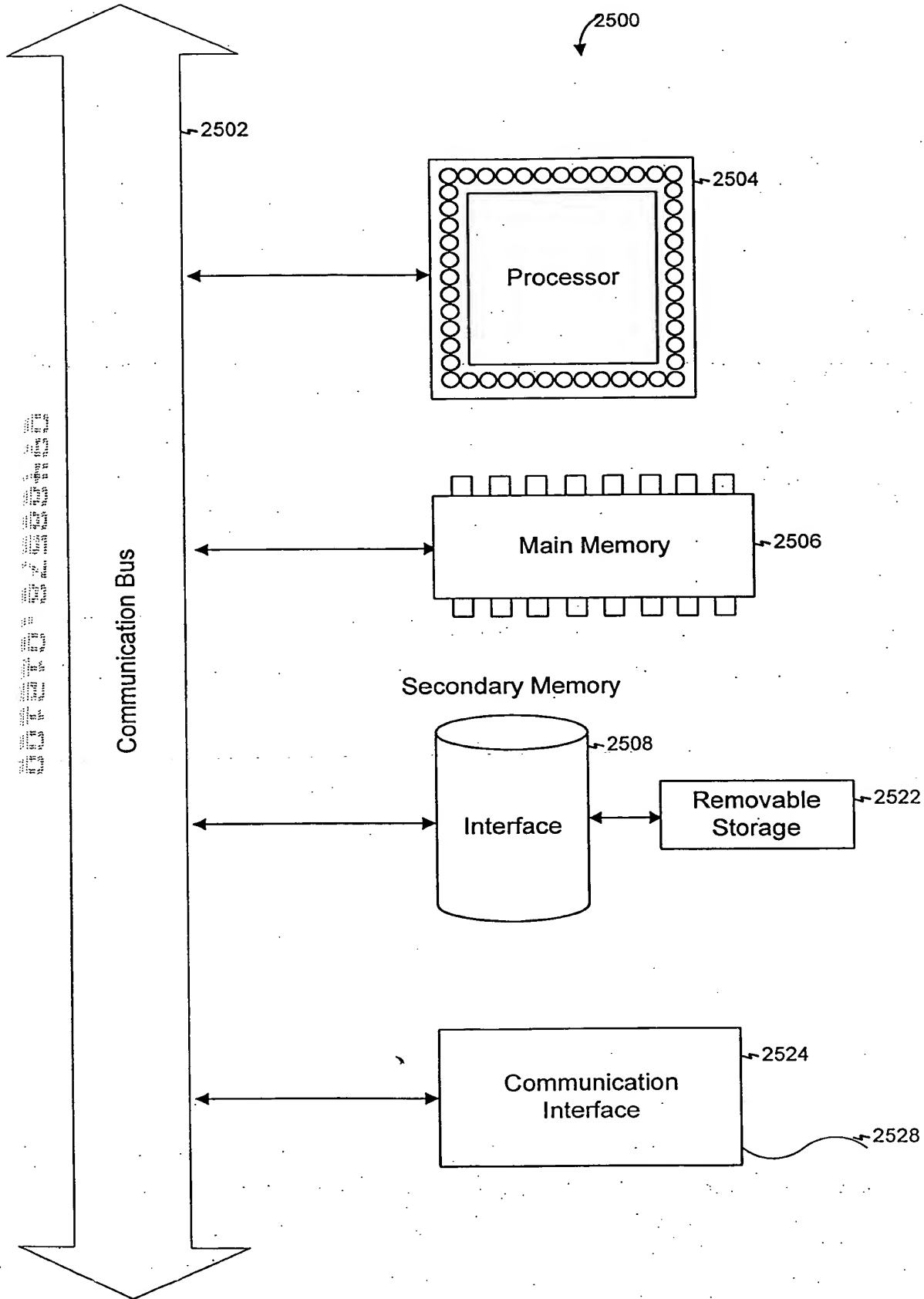


FIG. 25

GWF 133

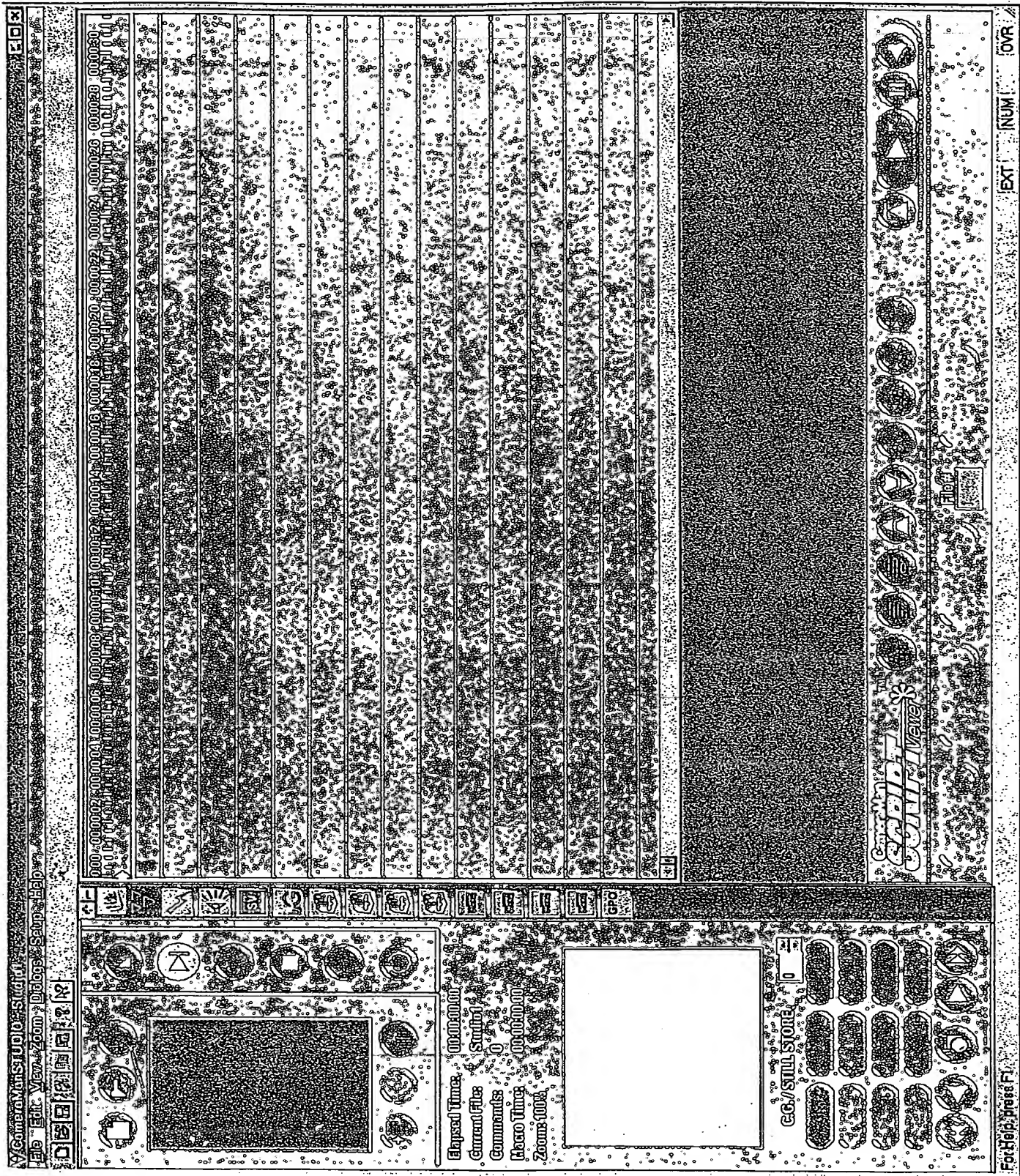


FIG. 27

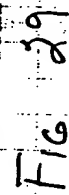
2800

Start time	00:00:00.00
End time	00:00:00.00
Difference	00:00:00.00
Insert time	Delete time
	Cancel

2804

2802

FIG. 28



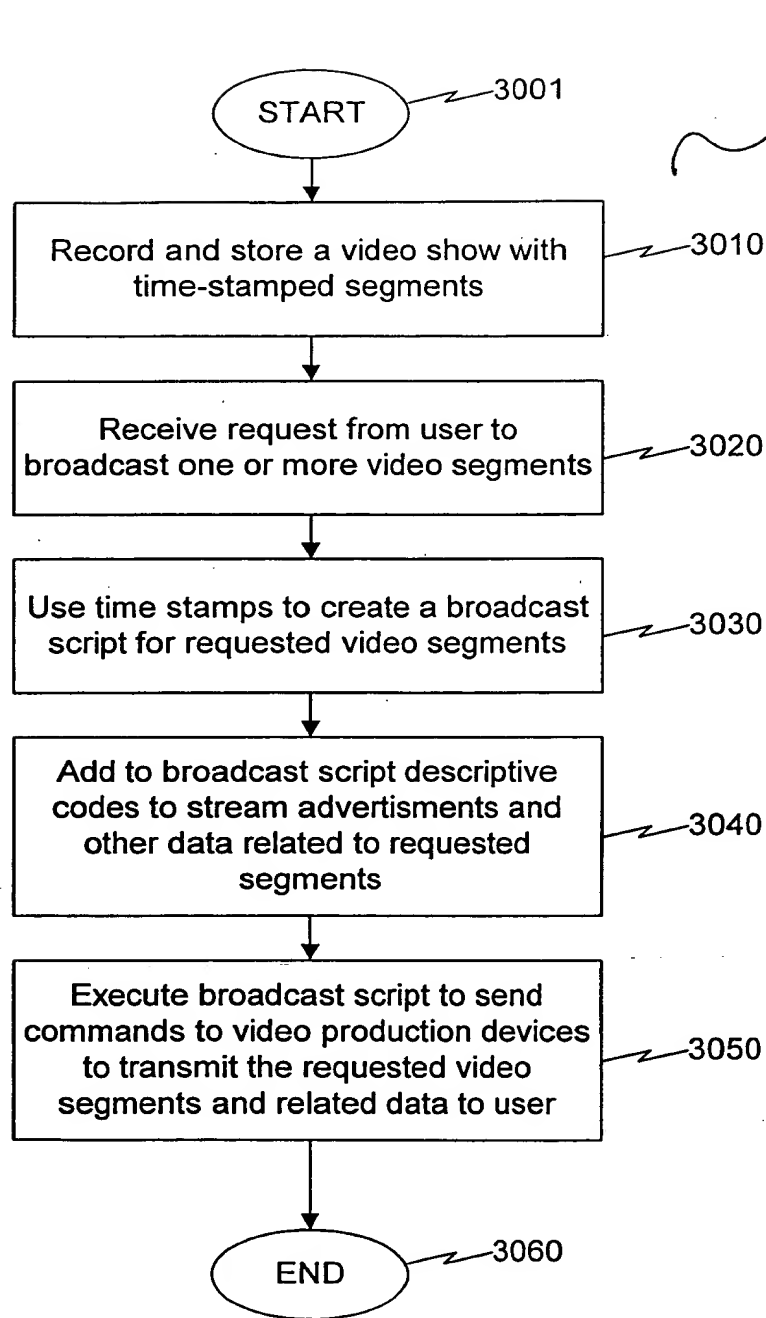


FIG. 30

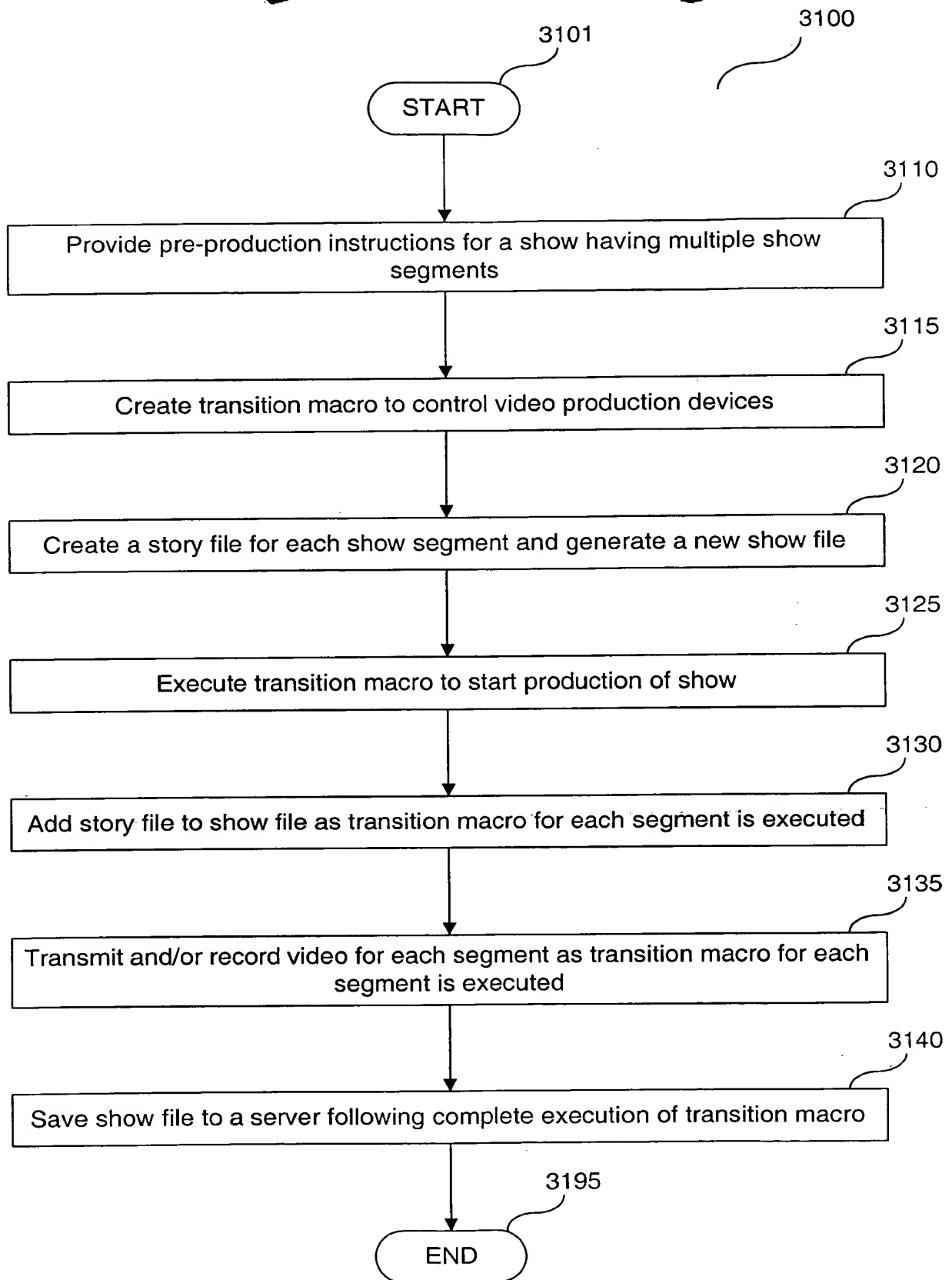


FIG. 31